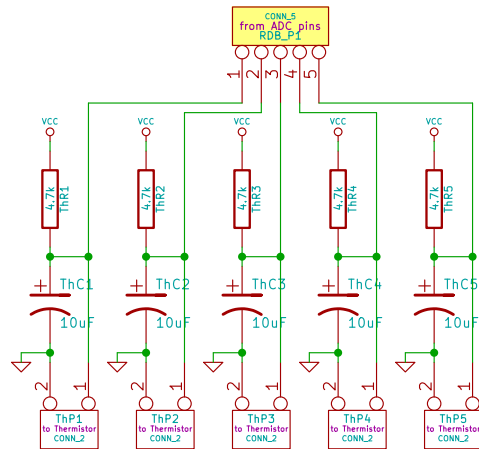
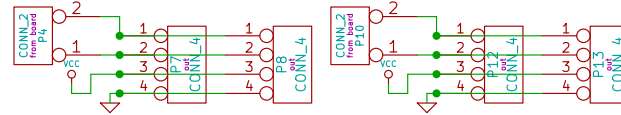


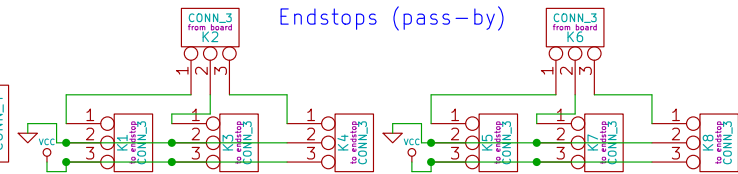
Thermistors



Comms sniffing (pass-by)



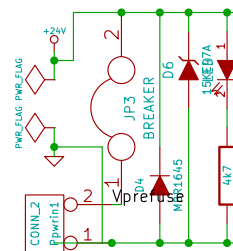
Endstops (pass-by)



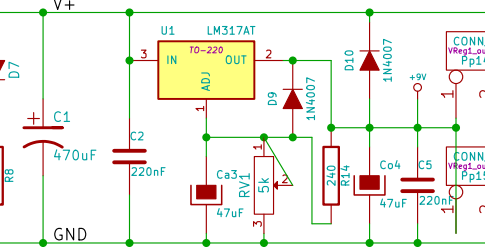
Holes

- Hole1
- Hole2
- Hole3
- Hole4
- HoleRad1
- HoleRad2
- HoleRad3
- HoleRad4

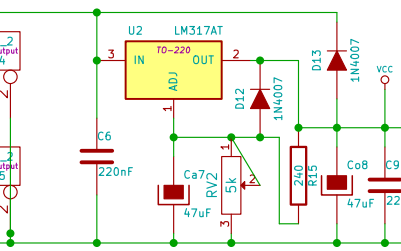
Power input



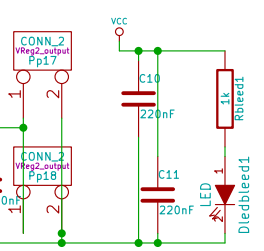
V.Regulator1: adj, board supply



V.Regulator2: adj, logic lvl



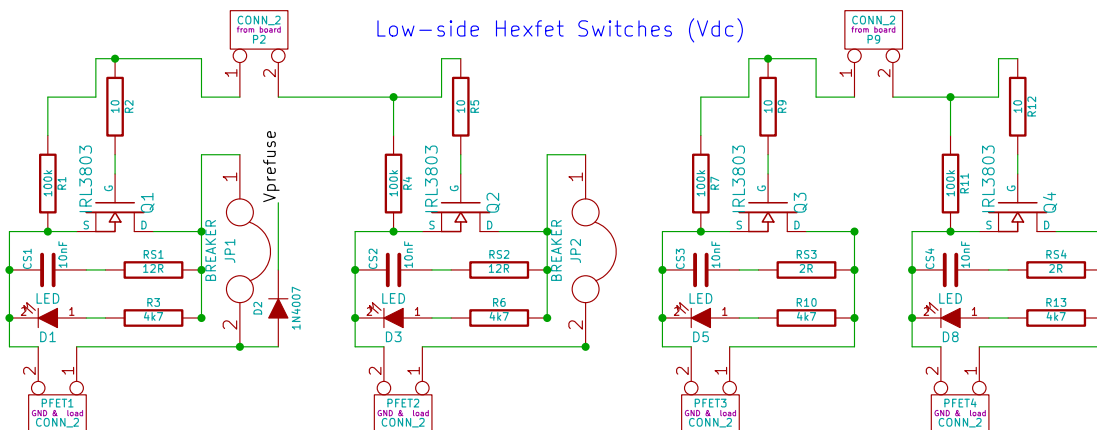
Power ends



MBR or SPST diode current > fuse, Vbr min = Vin*1.3
15KEA unidirectional Vbr min extra rating approx *1.2
For 40-56V inputs use LM317HV, transil 15KE68A, SPST/MBR1660

Set Voltage Regulator 2 for logic level voltage.
If development board has its own onboard regulator then match it.
Most regulators 3pin, adj, T0220 are compatible (LM1086), but double check.

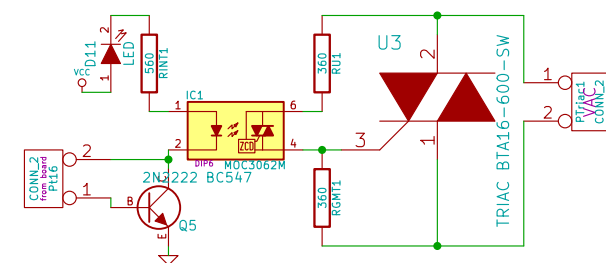
Low-side Hexfet Switches (Vdc)



Attention: snubbers generic values; CS preferable high voltage; check RS power dissipation

WARNING: these are low side switches.
Connect fet Source pin to PSU GND directly, and the fet Drain pin to the line coming from load. Load needs V+ directly from psu.

Triac Switch (Vac)



RINT = to give 5mA/moc3063, 10mA/moc3062 15mA/moc3061
RU = 360R /moc306x
RGMT = 360R /moc306x
optional snubber default values RS=39R CS=10nF/1kV/disc

RDB_G_001

www.reprap.org/RDB

File: RDB_G_001.sch

Sheet: /

Title: RDB_G_001alpha

Size: A4

Date: 13 apr 2013

Rev: 001alpha

KiCad E.D.A.

Id: 1/1